



OPEN Hardware IP protection by exploiting IP vendor's proteogenomic BioMarker as digital watermark during behavioral synthesis

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Handling the threats of intellectual property (IP) piracy and false IP ownership claim, are central from the perspective of IP vendor's right as well as reliability of system-on-chip (SoC) designs. IP design piracy has become an emergent concern for the digital hardware design community, in the last few years. This paper presents a novel behavioral synthesis based hardware IP Protection methodology that exploits IP vendor's proteogenomic Bio-marker as digital Watermark (BioW-IPP), for detective countermeasure against IP piracy and quashing false IP ownership claim. The proposed approach is capable of exploiting the IP vendor's bio-watermark (proteogenomic signature) as secret digital evidence (BW-ID) to generate a robust hardware watermark for embedding into the IP design during behavioral synthesis/high level synthesis (HLS) process. The generated proteogenomic signature bio-watermark is embedded into the register allocation phase of behavioral synthesis (HLS) that ensures negligible design overhead post-embedding. The proposed approach on comparison with prior behavioral synthesis based watermarking techniques achieved greater security in terms of lower probability of coincidence (watermark collision) and higher tamper tolerance, at nominal design overhead.

Keywords Proteogenomic, Bio-watermark, Hardware IP protection, Behavioral synthesis

In the field of electronics and multimedia systems, the efficient design of data and computation applications is critical for achieving enhanced performance. One of the most prominent computation intensive applications in the domain of image and video processing are joint photographic experts' group compression decompression (JPEG-CODEC) application, image processing filters, etc. These applications play a pivotal role in handling data-intensive tasks (that involve identifying and isolating key attributes or characteristics of an image), that are fundamental to modern multimedia applications. Given the computational complexity and high throughput requirements of these operations, there is an increasing need for the development of dedicated hardware solutions tailored to address these challenges efficiently. Such specialized hardware ensures optimized performance, reduced latency, and lower energy consumption. The design process of these hardware systems involves various abstraction levels, including (a) algorithmic level, (b) system level, (c) register transfer level (RTL), (d) gate level, and (e) layout level. High level synthesis is a design process that automatically transforms a design representation (application) from the algorithmic level to the RTL counterpart. These data intensive applications can be efficiently designed as dedicated reusable intellectual property core using HLS framework^{1,2}. Further, the electronic design industry relies heavily on the global integrated circuit supply chain of dedicated hardware IP core. As system-on-chip (SoC) design becomes more commoditized, the industry faces growing challenges, such as security vulnerabilities and escalating concerns about piracy, which pose significant risks to global supply chains. As the design and manufacturing processes often involve multiple parties across various geographical and organizational boundaries, they become susceptible to malicious activities by internal attackers within the SoC house. The potential hardware threats include IP piracy and unauthorized claim of IP ownership which may arise during the SoC integration or fabrication processes. Also, pirated/counterfeited IPs may contain malicious logic, resulting in risks such as functional malfunctions, overheating, data breaches, and reputational

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harm to original IP vendors. Additionally, IPs can be misused in various ways, including fraudulently claim by an adversary. From the perspective of original IP vendor, attacker might be present in the SoC house, which might be engaged into piracy and false claim of IP ownership rights. These situations can lead to significant financial losses, damage to the reputation of original IP vendor, and erosion of trust in the IP market design supply chain^{3–12}. IP piracy involves the unauthorized replication or use of proprietary IP, while false claims of ownership can result in legal disputes and undermine the rightful owner's ability to monetize their work.

In order to provide detective countermeasure/control against the aforementioned threats, security constraints (as IP vendor's digital evidence) can be implemented across various design process abstraction levels, such as algorithmic, RTL, gate level, and physical level. Leveraging HLS for watermarking offers security at higher abstraction levels while reducing complexity and cost. This approach enhances IP protection by providing detective control against IP piracy, and false IP ownership claim by embedding security measures early in the design process^{1,2}. HLS enables designers to integrate watermark constraints seamlessly during key stages of the design process, such as the register allocation and binding phase. This class of technique introduces minimal overhead to the overall design, ensuring that performance, area, and cost remain largely unaffected. This paper presents a novel hardware watermarking technique by employing proteogenomic bio-markers as robust digital watermark. The generated digital watermark is integrated as security/watermarking constraints into the hardware IP designs, which serve as robust and irrefutable evidence of original IP vendor. In cases of piracy or ownership disputes, these watermarks can be used as definitive proof of the rightful owner's claims, providing a solid foundation for legal action and resolution. By adopting such advanced security techniques, IP vendors not only safeguard their assets but also contribute to the overall integrity and trustworthiness of the hardware IP design process.

Novel contribution of this paper

1. This paper presents a novel hardware watermarking methodology that leverages the IP vendor's *proteogenomic biomarker* to provide detective countermeasure against IP piracy and false IP ownership claim.
2. The proposed approach is capable of exploiting the IP vendor's *bio-watermark (proteogenomic signature)* as secret digital evidence (BW-ID) to generate a robust hardware watermark. The proposed approach has been demonstrated on JPEG-CODEC IP core.

Related works

In the literature, there have been some prior works that have been attempted to secure IP cores against false claim of IP ownership and potential threats of IP piracy. This includes hardware watermarking^{3–16} facial biometric¹⁷, steganography¹⁸, DNA biometric¹⁹, handwritten signature²⁰ and digital signature²¹. The authors in¹³ proposed a hardware watermarking approach based on pragma insertion during the functional unit allocation phase of the HLS process. Additionally, the pragma insertion-based approach restricts the generation of large-size constraints, and the work lacks a comprehensive security analysis, including evaluations of watermark collision, resistance to brute-force attacks, and susceptibility to standard attack methodologies. Authors of¹⁷ proposed watermarking approach using facial biometric features of an IP vendor to generate watermark security constraints. Approach¹⁷ is incompetent of generating large watermark signature due to limited facial features. The watermarking approach described in¹⁴ employs a binary encoding technique to encode vendor's signature into watermarking constraints, that are incorporated into the design process after embedding. This approach is capable of producing good watermark strength, however, doesn't produce as strong watermark as the proposed approach in term of size and security. In¹⁵ authors proposed the method to embed digital signature bits within the hardware description language (HDL) design, utilizing message digest 5 (MD5) and secure hashing algorithm (SHA-1), but it leads to design area overhead. The approach in¹⁶ also uses SHA-1 and RSA at the HDL level, but it becomes vulnerable if the RSA key is revealed. This method relies entirely on the RSA key, as it is the sole security layer in the watermarking technique. Moreover, the approach in¹⁹ leverages the DNA biometric of IP vendors to generate security constraints. This approach is promising however, it doesn't generate watermark signature based on proteogenomic bio marker of an IP vendor. This results in lesser security in terms of tamper tolerance as compared to the proposed approach. The approach in²⁰ exploits the handwritten signature image of IP vendors to generate digital template, while the authors of²¹ proposed multi-level encoding and encrypted-hash-based digital signature for hardware protection. Both approaches^{20,21} generate lower watermark strength as digital evidence for IP protection. Similarly, the steganography-based approach in¹⁸ embeds secret marks through steganography constraints. However, its effectiveness diminishes if the design data or steganography encoding process is compromised, and the generation of steganography constraints adds significant implementation complexity. On the contrary, the proposed proteogenomic based watermarking employs the following several security layers, which an attacker needs to completely decode to regenerate the original watermark constraints to falsely claim IP ownership: (a) type of proteogenomic signatures (such as protein sequence signature and DNA sequence signature) provided by IP vendor, (b) proteogenomic signature fusion order to generate final watermark signature, (c) IP vendor's signature generation encoding rule, and (d) watermark constraints mapping/embedding rule.

The proposed methodology: BioW-IPP

Motivation and overview of proposed approach

Proteogenomic is a hybrid combination of protein molecular biometric and DNA biometric information of an individual. Proteogenomic can serve as an important bio-marker during forensic analysis and identification. There are only about 20 different types of amino acids in the human body, but these can combine to make approximately 20,000 unique proteins. The order of amino acids in the polypeptide chain is determined by

the order of nucleotides (the DNA sequence) of the gene that encodes it. Even a tiny change in the amino acid sequence of the polypeptide chain can alter the overall structure and function of the protein. This diversity of amino acids and the sheer number of possible combinations in their linear order allow for tremendous dissimilarity in the properties of proteins. The choice of amino acid sample (protein sample) augmented with the peptide chain (fragment) strength dictates the uniqueness generated from an IP vendor's perspective²². Exploiting the formation of a robust integrated proteogenomic signature post-extraction of protein biometric signature and DNA signature from its respective samples, creates an exclusive, inimitable and imperative piece of digital bio-evidence that can be leveraged to generate a sturdy hardware watermark. *Note:* The respective sample of the IP vendor is taken after due consent of the respective entity and the obtained DNA/protein sequence from the lab is safely stored in a secure database. There will be a non-disclosure agreement (NDA) with the respective lab to preserve the privacy of the entity. Thereafter, obtaining/extracting the DNA/protein signature (to form proteogenomic signature) after receiving the respective sequences, is a straight-forward process using the flow in (Fig. 1). As established in several works earlier^{17,19,23,24}, usage of biometric identifier is not extracted from a regular employee, but rather from an entity of the life board of trustees/owner/founder (who is also a trustworthy insider in the IP vendor's house). In the proposed hardware watermarking approach, we biomimic the generation of proteogenomic signature from an IP vendor's body sample for forming its respective bio-watermark as secret digital evidence (BW-ID). This BW-ID is embedded covertly in a hardware IP for providing detective countermeasure against IP piracy and false IP ownership. Our BW-ID enables us to create a powerful hardware watermarking framework. Even though the proposed approach combines the knowledge of extracting and encoding proteogenomic signature, however, it only requires the involvement of sequencing labs for providing the IP vendor with his/her DNA sequence and protein sequence respectively. Once the DNA sequence and protein sequence are received, then an IP vendor can algorithmically generate the alpha/beta chain protein signatures and DNA signature respectively for watermark generation, using the proposed flow in (Fig. 1). There is no additional knowledge of bioinformatics required for hardware watermark constraint generation. Therefore, the proposed proteogenomic signature-based watermark constraint generation process can be applied to any hardware IP designs with the knowledge of high-level synthesis design process and proposed watermark generation algorithm (Fig. 2).

The paper presents a novel HLS methodology to secure hardware IP cores using BW-ID of authentic IP vendor. This gives a distinctive molecular level identification of the authentic IP provider. These bio-markers are taken from her/his body sample (insulin and DNA). The primary inputs of the proposed secure HLS framework (in Fig. 1) consist of the module library, data flow graph (DFG) or control data flow graph (CDFG) of the hardware application, resource constraints, scheduling algorithm, protein sequence of IP vendor (alpha chain and beta chain), DNA sequence of IP vendor, watermark encoding mechanism (watermark constraint generator), and fusion order to generate proteogenomic based watermark. This proposed secure HLS flow consists of four main sections, viz. (i) protein signature generation from alpha chain (1st polypeptide chain), (ii) DNA signature generation from DNA sequence, (iii) protein signature generation from beta chain (2nd polypeptide chain), and (iv) HLS design process. After applying IP vendor's provided fusion order, an integrated proteogenomic digital watermark template (BW-ID) is generated. Further, by using the watermark encoding rule of the IP vendor, the generated watermark template is converted into its respective hidden security constraints. Based on the primary inputs, different HLS steps of scheduling, allocation, binding, and register allocation are performed, followed by construction of the initial register allocation table (RAT), which describes the assignment of initial storage variable into distinct registers for the given hardware application (DFG). Subsequently, the previously generated watermark security constraints are integrated into the register allocation phase of HLS process for the target hardware application. Finally, secured register transfer level (RTL) hardware design is generated carrying proteogenomic watermark impression. This design contains the IP vendor's robust watermark signature that can provide detective countermeasure against IP piracy and resolution of false IP ownership. Detailed explanations of the proposed approach are given in the next section.

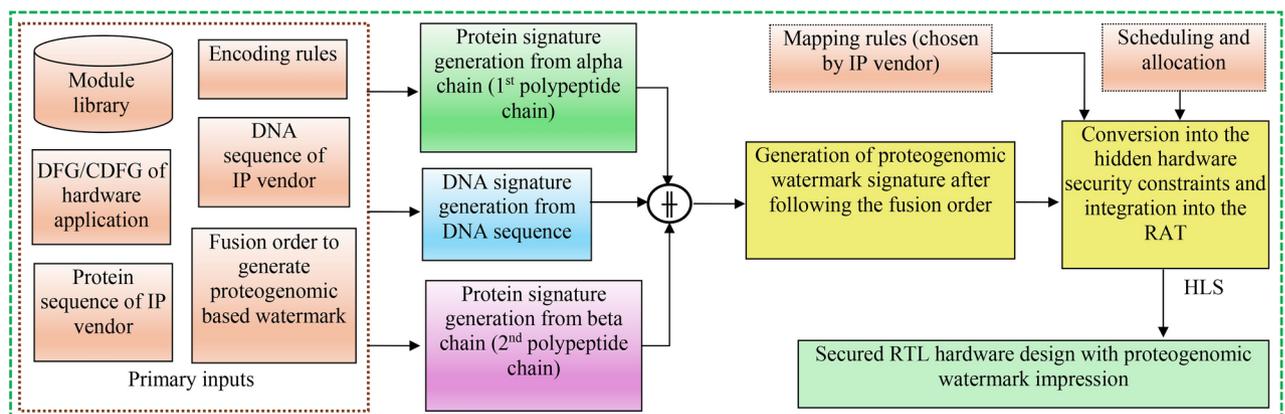


Fig. 1. Overview of the proposed methodology: BioW-IPP.

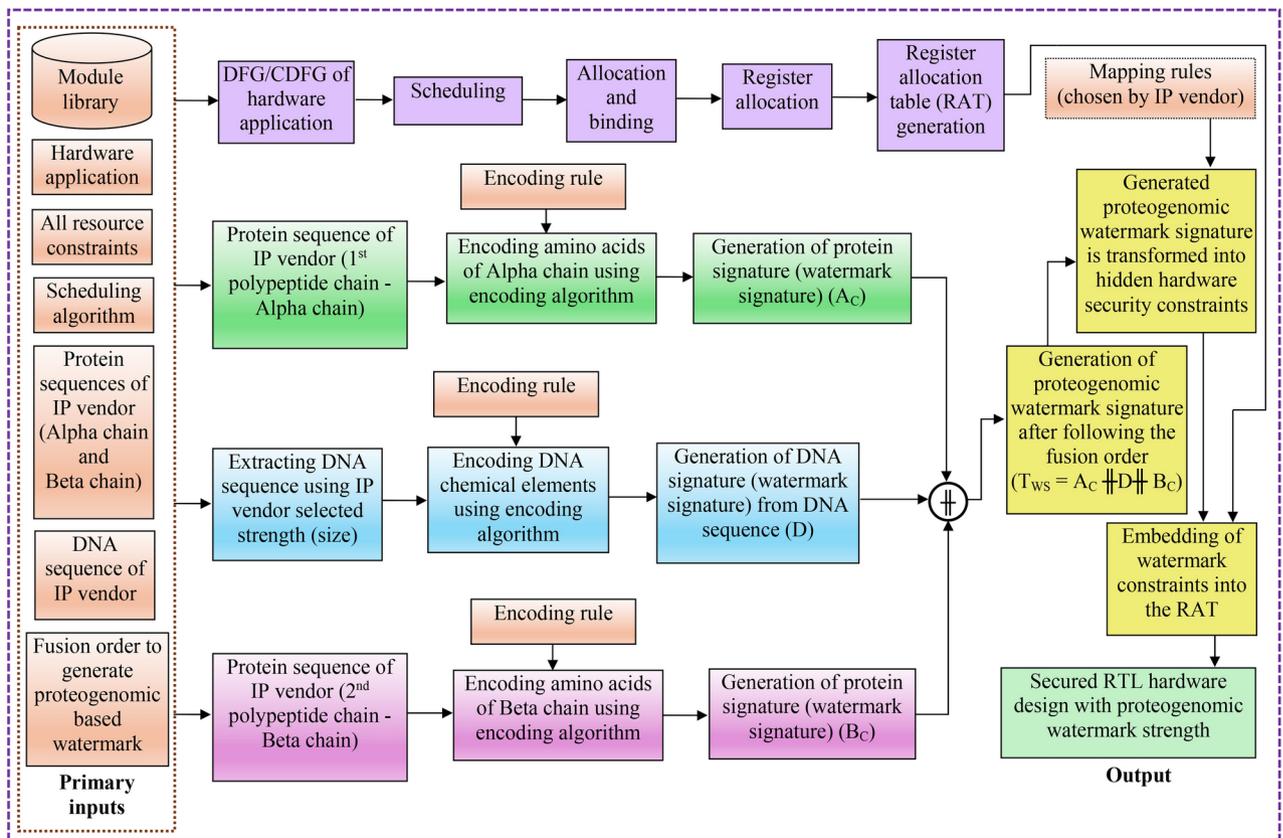


Fig. 2. Details of the proposed methodology: BioW-IPP.

Details of the proposed methodology

The proposed methodology to generate proteogenomic digital watermark for securing hardware IP cores against IP piracy/false claim of authentic IP ownership are demonstrated in detail through the following modules (section III.B.1 – III.B.5). The secured HLS design flow of the proposed approach is explained in (Fig. 2).

Protein watermark signature generation from alpha chain—1st polypeptide chain

The structure of human body protein (extracted from insulin) consists of a sequence chain of amino acids. The connection between these amino acids is formed by bonding the carboxylic acid group of one amino acid with the amine group of another amino acid, which is known as a peptide bond. Peptide bonds link amino acids together to create a series of polypeptide chains or a protein sequence. As shown in (Fig. 3), there are two different protein sequences (polypeptide chain) of IP vendor's human body insulin. 1st polypeptide chain is named/termed as '*Alpha chain*', and second polypeptide chain is named/termed as '*Beta chain*'. There are nearly 500 amino acids in the nature, but in human protein sequence, there are 20 unique amino acids^{25,26} (in Fig. 3) which link together to create the amino acid chain. Figure 2 explains the process of generation of digital watermark (A_c) from its respective *alpha chain*. Based on the IP vendor's protein sequence (as input), each amino acid is encoded into unique encoding bit using the proposed encoding rule, which subsequently after concatenation of the binarized bits generates a unique protein watermark signature (A_c). Further, Fig. 4 shows the encoding of the 1st polypeptide chain, which consists of 21 amino acids (*Note: The shaded protein elements in the table are not part of the alpha chain*). Each amino acid is encoded uniquely according to their positions in the alphabet (encoding rule), like, the amino acid Glycine (G) has an alphabet position of 7 and is encoded with binary bits 111. Similarly, for Isoleucine (I), the alphabet position is 9 and corresponding binary representation is 1001. Finally, ' A_c ' is generated by combining the binarized bits that represent the amino acids in the alpha chain (as shown in Fig. 4). Finally, the total size of watermark strength from IP vendor's 1st polypeptide chain is 81 bits.

DNA signature generation

In the proposed approach, chromosomal *DNA sequence* contains two base pairs (BP). First BP is generated with two chemical elements (T-Thymine and A-Adenine), and second BP is generated with other two chemical elements (G-Guanine and C-Cytosine). Subsequently, DNA sequence can be generated in two ways, either taking alternative base pairs of similar type (like, TA-AT-GC-CG) or taking alternative base pairs of distinct type (like, TA-GC-AT-CG). In our proposed approach, combinations of base pairs of similar type are assumed in the DNA sequence. The polynucleotide (sugar phosphate backbone—represented as 'S') has been included as both the leading and lagging strands in the DNA sequence to create the final DNA sequence. Figure 2 explains the process of generation of digital watermark (D) from the respective IP vendor's DNA sequence. Based on the

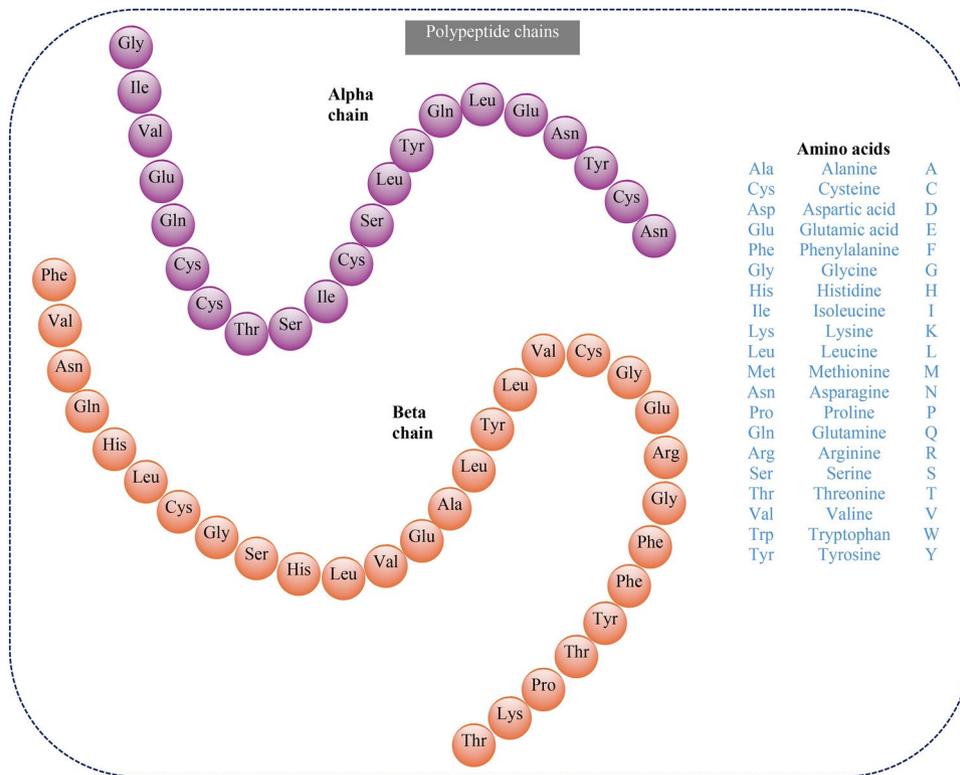


Fig. 3. Protein (polypeptide chains) of IP vendor’s human body insulin.

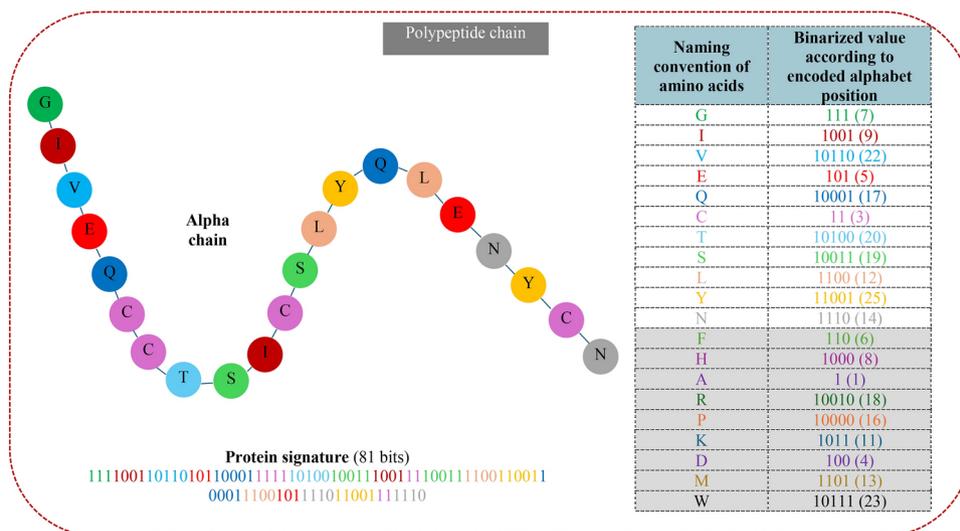


Fig. 4. Alphabet substitution and watermark signature corresponding to the amino acid sequences of 1st polypeptide chain.

IP vendor’s DNA sequence (as input), each chemical elements are encoded into unique encoding bit using the proposed encoding rule (according to their positions in the alphabet), which subsequently generates a unique DNA watermark signature (D). IP vendor’s DNA sequence (STAS-SATS-SGCS-SCGS-STAS-SATS-SGCS-SCGS-STAS-SATS-SGCS-SCGS-STAS-SATS-SGCS-SCGS) is depicted in (Fig. 5) (adopted from¹⁹). Subsequently, it is converted into digital template using the encoding rule. For example, the chemical element Guanine (G) has an alphabet position of 7 which is encoded with binary bits 111. Similarly, for Polynucleotide (S), the alphabet position is 19 which is encoded with binary bits 10011. Finally, the corresponding encoded DNA watermark signature (D) is shown in Fig. 5 where the total size of watermark strength is 248 bits.

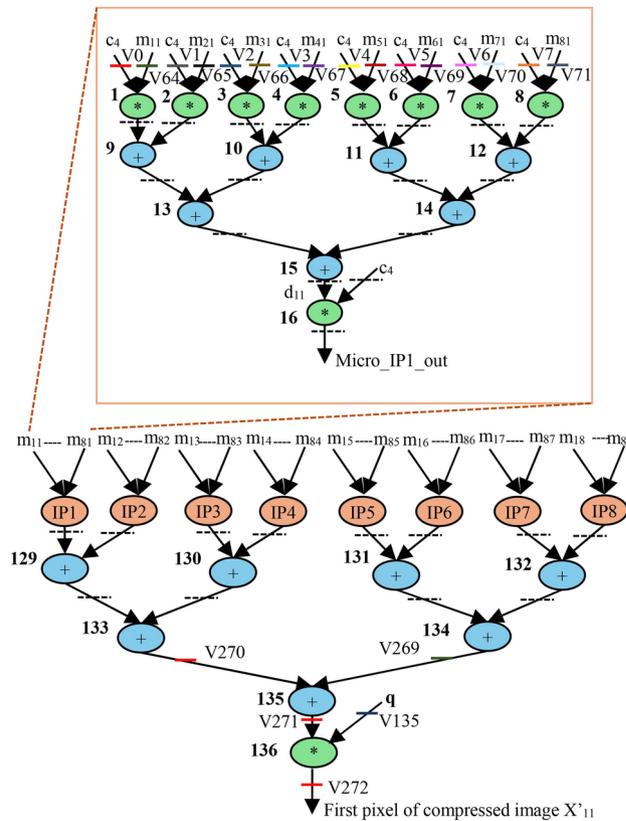


Fig. 7. DFG of JPEG-CODEC algorithm to determine each pixel of the compressed image X'_{11} .

quantization procedure as well. To perform DCT on a given image input block, the 2D-DCT coefficient matrix (T) is applied to the input block (M) using the following formula²⁷:

$$X = D * T^{trans} \tag{1}$$

where, 'D' is calculated through (2)

$$D = T * M \tag{2}$$

Elements of 'D' matrix ($d_{11}, d_{12}, \dots, d_{88}$) indicates column wise transformed elements of input image block. Further, elements of 'X' matrix ($X_{11}, X_{12}, \dots, X_{88}$) indicates both row and column wise transformed elements of input image block. Thus, T^{trans} is the transpose of 2D-DCT coefficient matrix 'T', and 'X' is the corresponding discrete cosine transformed block of input image block M.

For a dedicated IP core design, the matrix relationship must be transformed into a hardware function, for that reason—the pixels (m_{ij}) of the input image block (M) are transformed into compressed image pixel (X_{ij}) (using (1)). For example, X_{11} which is the first pixel of the compressed image is modeled as follows²⁷:

$$X_{11} = c_4 * d_{11} + c_4 * d_{12} + c_4 * d_{13} + c_4 * d_{14} + c_4 * d_{15} + c_4 * d_{16} + c_4 * d_{17} + c_4 * d_{18} \tag{3}$$

where, $d_{11}, d_{12}, \dots, d_{18}$ is calculated as follows:

$$d_{11} = c_4 * m_{11} + c_4 * m_{21} + c_4 * m_{31} + c_4 * m_{41} + c_4 * m_{51} + c_4 * m_{61} + c_4 * m_{71} + c_4 * m_{81} \tag{4}$$

$$d_{12} = c_4 * m_{12} + c_4 * m_{22} + c_4 * m_{32} + c_4 * m_{42} + c_4 * m_{52} + c_4 * m_{62} + c_4 * m_{72} + c_4 * m_{82} \tag{5}$$

Similar transformations are made to the other pixels of the block (M), where the input pixels stay the same, but the 2D-DCT coefficients change. Therefore, the equation's structure stays the same, but the inputs used to calculate the various modified image pixel intensities alters. Each macro-IP is created using eight structurally similar micro-IPs (named IP1-IP8), and each micro-IP uses (4)-(5) to perform its d_{ij} . Every micro-IP operation, multiplication, and addition operation are represented by light brown, green, and blue color nodes respectively. As illustrated in (Fig. 7), the result of operation 135 produces the pixel intensity of the transformed image (X). All the scheduling, binding and allocation operations of the JPEG-CODEC are shown in (Table 1).

After the DCT transformation, the pixel intensities of the 8×8 image block (X) are subsequently compressed using quantization technique. To obtain various quality levels (from 1 to 100), a distinct quantization matrix is utilized. Quality level 100 signifies reduced compression resulting in a higher quality image, while quality level

C.S	Oper. assigned to M1	Oper. assigned to M2	Oper. assigned to M3	Oper. assigned to A1	Oper. assigned to A2	Oper. assigned to A3
1	1	2	3	–	–	–
2	4	5	6	9	–	–
3	7	8	17	10	11	–
4	18	19	20	12	13	–
5	21	22	23	25	26	14
6	24	33	34	27	29	15
7	35	36	37	28	41	–
8	38	39	40	42	30	–
9	49	50	51	43	44	45
10	52	53	54	57	46	31
11	55	56	65	58	59	47
12	66	67	68	60	61	–
13	69	70	71	73	74	62
14	72	81	82	75	77	63
15	83	84	85	76	89	–
16	86	87	88	90	78	–
17	97	98	99	91	92	93
18	100	101	102	105	94	79
19	103	104	113	106	107	95
20	114	115	116	108	109	–
21	117	118	119	121	122	110
22	120	16	32	123	125	111
23	48	64	80	124	129	–
24	96	112	–	126	130	–
25	–	–	–	127	131	133
26	–	–	128	–	–	–
27	–	–	–	132	–	–
28	–	–	–	–	134	–
29	–	–	–	–	–	135
30	–	–	136	–	–	–

Table 1. Scheduling, binding and allocation of DFG operations for JPEG-CODEC benchmark.

0 signifies increased compression resulting in a lower quality image. This is accomplished by operation 136, which is executing the quantization on the transformed image pixel intensities (X) using the matrix element ' q ' of the quantization matrix (Q). This generates the final output as compressed/quantized image pixel values (X'). The next sub-section of this paper utilizes JPEG-CODEC SDFG to derive the register allocation information for watermark constraints generation and its subsequent embedding.

Final digital template and conversion into security constraints, and its embedding

The final proteogenomic based digital watermark signature is generated after following the IP vendor's provided fusion order i.e., the concatenation of ' A_C ', ' D ' and ' B_C ' ($T_{WS} = A_C \# D \# B_C$) accordingly. Consequently, this watermark signature is converted into the watermark (security) constraints using IP vendor's chosen mapping/embedding rule.

Watermark mapping/embedding rule If the proteogenomic digital watermark signature bit is 0, then even-even storage variable pairs $\langle V_i, V_j \rangle$ from the SDFG are generated as watermarking constraints, where ' i ' and ' j ' are the storage variable numbers. In contrast, for an encrypted watermark signature bit 1, the generated constraints are odd-odd storage variable pairs in the SDFG.

This indicates that the mapping of the watermark constraints happens during the register allocation phase of the HLS process. In the JPEG-CODEC DFG (shown in Fig. 7), different registers (used to store variables) are represented with different colors, and storage variables are represented as V0-V272. In SDFG, storage variables are used for storing the primary and intermediate inputs/outputs of the operations temporarily. The SDFG of JPEG-CODEC is used to generate the corresponding RA table, which shows the optimal allocation of storage variables to registers (using register sharing). The following section provides a detailed explanation of how to create watermark security constraints using the proteogenomic digital watermark and its embedding (explained with the same JPEG-CODEC example). In the proposed method, final proteogenomic based digital watermark (T_{WS}) obtained is shown in (Fig. 8), where total size of watermark strength is 445 bits (which consists of 168 zeros and 277 ones). These 445 bits are mapped and embedded as watermark constraints in the RA table of the IP design (using the above mapping rule). Therefore, the watermark constraints obtained for JPEG-CODEC

	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11	R12	R13	R14	...	R136	R137
C0	V0	V1	V2	V3	V4	V5	V6	V7	V8	V9	V10	V11	V12	V13	...	V135	V136
C1	V137	V138	V139	V3	V4	V5	V6	V7	V8	V9	V10	V11	V12	V13	...	V135	-
C2	V201	-	V139	V140	V141	V142	V6	V7	V8	V9	V10	V11	V12	V13	...	V135	-
C3	V201	-	V202	-	V203	-	V143	V144	V145	V9	V10	V11	V12	V13	...	V135	-
C4	V233	-	-	-	V203	-	V204	-	V145	V146	V147	V148	V12	V150	...	V135	-
C5	V233	-	-	-	V234	-	-	-	V205	-	V206	-	V149	-	...	V135	-
C6	V249	-	-	-	-	-	-	-	V235	-	-	-	V207	-	...	V135	-
C7	V249	-	-	-	-	-	-	-	V235	-	-	-	V207	-	...	V135	-
C8	V249	-	-	-	-	-	-	-	V235	-	-	-	V236	-	...	V135	-
C9	V249	-	-	-	-	-	-	-	V235	-	-	-	V236	-	...	V135	-
C10	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C11	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C12	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C13	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C14	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C15	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C16	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C17	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C18	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C19	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C20	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C21	V249	-	-	-	-	-	-	-	V250	-	-	-	-	-	...	V135	-
C22	V257	-	-	-	-	-	-	-	V258	-	-	-	-	-	...	V135	-
C23	V265	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C24	V269	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C25	V269	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C26	V269	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C27	V269	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C28	V269	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C29	V271	-	-	-	-	-	-	-	-	-	-	-	-	-	...	V135	-
C30		V272	-	-	-	-	-	-	-	-	-	-	-	-	...	-	-

Table 2. Register allocation table (rat) corresponding to JPEG-CODEC benchmark.

Results and analysis

This section analyses the proposed methodology in terms of the impact of proposed BW-ID watermark feature on achieved watermark strength (used as digital proof/evidence), followed by its security potency (robustness against IP piracy), and design overhead. The experimental evaluation of the proposed methodology is carried out on a 1.80 GHz processor and 8 GB memory. For area and latency estimation, the proposed methodology employs a 15 nm technology based on the NanGate library²⁸.

Analysis on final digital watermark signature generation for BW-ID feature

In the proposed approach, final digital watermark signature is generated from BW-ID features (DNA and protein). DNA sequence contains 248 bits watermark strength and protein sequence contains 197 bits watermark strength. These 445 bits watermark strength are mapped and embedded as watermark constraints in the RA table of the IP design (using the above mapping/embedding rule). The storage/register comparison before embedding and after embedding (for JPEG-CODEC benchmark) is shown in (Table 3). It is observed that there is no register overhead in the JPEG-CODEC benchmark for embedding the proteogenomic based watermark.

Security analysis

The results of the proposed approach have been evaluated on different designs such as JPEG-CODEC, Blur filter, Sharpening filter, LED filter, and Cardiac-Pacemaker, adopted from^{29,30}. The security analysis of the proposed methodology has been assessed using two important security metrics: *probability of coincidence* (PC) and *tamper tolerance* (TT)^{18,21,31–33}. The PC (also indicates false positive) is an important metric for evaluating the security robustness of a system, particularly in terms of how likely an IP vendor's watermark may be present in an unsecured IP design. It is also a measure of IP owners' digital proof that can be used to nullify the false IP ownership during litigation in IP court. A lower PC value indicates stronger security, which signifies a lesser likelihood of coincidentally matching the watermark constraints in an unsecured design (watermark collision). The PC is evaluated through the following equation^{18,21,32,33}:

Proposed features	Feature name	Feature strength (binary strength)	Total watermark strength	JPEG-CODEC		Register overhead
				Registers (pre-embedding)	Registers (post-embedding)	
DNA sequence	DNA (D)	248 bits	248 bits	137	137	0%
Protein sequence	Alpha chain (A _C)	81 bits	(A _C + B _C) = 197 bits			
	Beta chain (B _C)	116 bits				
Total (D + A _C + B _C)			445 bits	137	137	

Table 3. Registers comparison (pre-embedding vs. post-embedding) for JPEG-CODEC benchmark for the proposed approach.

Watermark constraints (bits)	Registers	PC
400	137	5.34E-2
410	137	4.96E-2
425	137	4.44E-2
435	137	4.13E-2
445	137	3.84E-2

Table 4. Calculated values of PC for the proposed approach corresponding to the JPEG-CODEC benchmark.

Benchmarks	Watermark constraints (bits)	PC
JPEG-CODEC	445	3.84E-2
Blur filter	339	2.81E-8
Sharpening filter	378	2.31E-8
LED filter	156	9.53E-6
Cardiac-pacemaker	445	2.20E-5

Table 5. Calculated values of PC for the proposed approach corresponding to the benchmarks.

$$PC = (1 - (1/r))^c \quad (6)$$

where, 'r' stands for the total number of utilized unique registers in the IP design before embedding watermark constraints, while 'c' represents the total watermark constraints embedded. Table 4 reports the probability of coincidence for the proposed approach corresponding to JPEG-CODEC benchmark for different values of digital watermark constraints. It shows that the value of constraints size, which is desirable for yielding robust security. Further, the values of PC for different benchmarks corresponding to the proposed approach are depicted in (Table 5).

Next, TT metric is the critical measure of secure design's robustness against tampering attack which is launched by an adversary. An increment in TT value signifies the generation of more signature combinations (search space), diminishing an attacker's chances of decoding the precise signature combination and identifying the original secret watermarking constraints. This higher TT value enhances the system's resilience against tampering. It is evaluated through the following equation^{21,31-33}:

$$TT = v^c \quad (7)$$

where, 'v' is the number of distinct embedding variables (like 0 and 1), and 'c' represents the total generated watermark constraints. Furthermore, Table 6 depicts the comparison of PC and TT among the proposed method and prior works^(13-21,34). The proposed method shows more robust security with a lesser value of PC and higher value of TT than all similar state-of-the-art methods. This is because the proposed proteogenomic based digital watermark generation method can generate and embed larger watermark constraints compared to prior methods. As the proposed bio-hardware watermark method involves several security layers such as protein sequence signature generation and DNA sequence signature generation, proteogenomic signature fusion order, and watermark constraints mapping/embedding rule etc., therefore, the proposed methodology makes it harder for an attacker to exactly regenerate the embedded digital watermark security constraints or tamper with the proposed embedded digital watermark by exactly decoding the watermark security constraints. Overall, these aforementioned reasons increase the robustness of the proposed approach against IP piracy and fraudulent claims of IP ownership.

Design cost analysis

The overhead/design cost analysis of the proposed method is evaluated using the following metric^{21,31-33}:

Different techniques	Watermark constraints (bits)	JPEG-CODEC		Cardiac-pacemaker	
		PC	TT	PC	TT
Proposed approach	445	3.84E-2	9.09E+133	2.20E-5	9.09E+133
Handwritten signature, 2023 ²⁰	269	1.39E-1	NA	1.53E-3	NA
FSM watermarking, 2022 ³⁴	128	3.92E-1	3.40E+38	4.57E-2	3.40E+38
DNA biometric, 2022 ¹⁹	128	3.92E-1	3.40E+38	4.57E-2	3.40E+38
Facial biometric, 2021 ¹⁷	83	5.44E-1	9.67E+24	1.35E-0	9.67E+24
Pragma based watermarking, 2021 ¹³	71	5.94E-1	NA	1.80E-1	NA
IP steganography, 2019 ¹⁸	203	2.26E-1	NA	7.50E-3	NA
Digital signature, 2019 ²¹	240	1.72E-1	1.77E+72	3.07E-3	1.77E+72
Watermarking, 2011 ¹⁶	256	1.53E-1	1.16E+77	2.09E-3	1.16E+77
Automated watermarking, 2008 ¹⁵	160	3.10E-1	1.46E+48	2.11E-2	1.46E+48
Watermarking, 2005 ¹⁴	240	1.72E-1	1.77E+72	3.07E-3	1.77E+72

Table 6. Comparison of PC and TT of the proposed approach with similar approaches for JPEG-CODEC and cardiac-pacemaker benchmark.

Watermark strength (bits)	Registers pre-embedding	Registers post-embedding	Design cost pre-embedding	Design cost post-embedding	% Overhead
100	14	15	0.7100	0.71026	0.037
125					
135					
145					
156					

Table 7. Design cost pre and post embedding watermark for led filter benchmark.

Watermark strength (bits)	Registers pre embedding	Registers post embedding	Design cost pre embedding	Design cost post embedding	% overhead
400	137	137	0.218	0.218	0
410					
425					
435					
445					

Table 8. Design cost pre and post embedding watermark for JPEG-CODEC benchmark.

$$\text{Design Cost} = W_1 (L/L_{\max}) + W_2 (A/A_{\max}) \quad (8)$$

where, ‘ W_1 ’ and ‘ W_2 ’ represent the weightage factors for normalizing the watermarked design latency and area (in the proposed approach, $W_1 = W_2 = 0.5$). ‘ L ’ and ‘ A ’ represent the watermarked design latency and area corresponding to the hardware. Further, ‘ L_{\max} ’ and ‘ A_{\max} ’ represent the corresponding maximum latency and maximum area. *Note:* The area and latency design metrics of the watermarked IP designs indicate the design area and latency after embedding the digital watermark signature of the IP vendor. Tables 7 and Table 8 depict the registers needed in the IP design before and after the embedding watermark constraints and simultaneous design cost for the proteogenomic based proposed approach corresponding to the LED filter and JPEG-CODEC benchmarks. The proposed proteogenomic based watermark incurs very less (0.037% only) design cost overhead (in terms of register count) for the LED filter IP design because of its smaller size, but it does not incur any design overhead for larger size JPEG-CODEC IP design after embedding. The register count indicates the quantity of storage hardware utilized during the HLS register binding phase (illustrated in Table 2 earlier, which shows the requirement of 137 registers, i.e., R1-R137) of the proposed approach, which subsequently reveals the number of storage variables in the HLS-generated RTL data path of the watermarked IP core. The details of the security/watermark strength generated corresponding to different proteogenomic feature types have been already discussed earlier in section III.B.5 and V.A. These registers generally hold the primary inputs, intermediate outputs of the data processing computation, and final output.

Further, Fig. 9 demonstrates the probability of coincidence vs. design cost trade-off for different watermark constraints corresponding to JPEG-CODEC application for the proposed approach. As shown in this figure, with the increment in the watermark constraints, the PC value decreases without occurring any design cost overhead, because the JPEG-CODEC is a very large size IP design which includes several registers in the datapath.

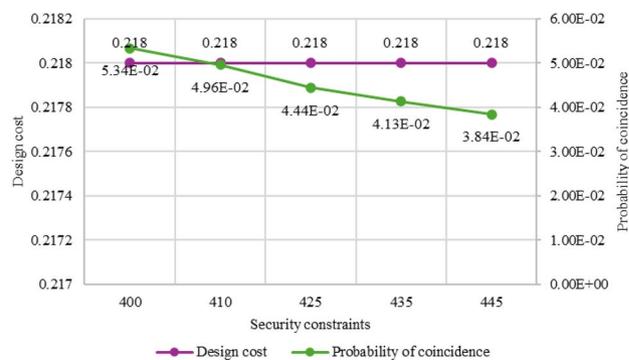


Fig. 9. Design cost vs. probability of coincidence trade-off for different number of watermark constraints of JPEG-CODEC application.

Benchmarks	Computational time (ms)
JPEG-CODEC	~ 1377
Blur filter	~ 1058
Sharpening filter	~ 1076
LED filter	~ 1052
Cardiac-pacemaker	~ 1108

Table 9. Total computational time of the proposed approach.

Table 9 reports the total computational time corresponding to each benchmark, when executed on a system with 1.6 GHz and 8 GB RAM. Computational time contains the process of protein signature and DNA sequence extraction, encoding, and integration; followed by watermark constraints generation and embedding time of the benchmark. As evident from Table 9, the computational time overhead is very low despite the steps involved for proposed watermark signature generation and embedding.

Limitations of the proposed work

There are some limitations of this proposed work:

- (i) In the process of converting watermark constraints from the proteogenomic based watermark signature (during HLS), it is essential to carefully generate the storage variable pairs (sorted in ascending order) that correspond to each bit of the watermark signature. If storage variable pairs are not generated carefully as watermark constraints, there is a risk of improperly mapping of signature bits to storage variable pairs.
- (ii) The embedding process of the proposed proteogenomic based watermark constraints requires careful local alterations/swapping of storage variables to the corresponding registers during behavioral synthesis process. Without proper precautionary swapping, there is a risk of register conflicts during watermark embedding.

Conclusion and future work

This paper presented a novel hardware IP protection methodology by exploiting IP vendor's proteogenomic biomarker to generate a robust digital watermark based digital evidence (BW-ID), for detective countermeasure against IP piracy. The proposed methodology makes it harder for an attacker to exactly regenerate the embedded digital watermark security constraints or tamper with the proposed embedded digital watermark by exactly decoding the watermark security constraints due to several security layers involved. This proposed method has performed better compared to similar prior works in terms of robust security, at negligible design overhead.

In our future work, we intend to explore low-cost proteogenomic bio-watermark for hardware IP protection of loop based applications based on simultaneous exploration of loop unrolling factor and resource configuration (with respect to design area-delay tradeoff).

Data availability

The datasets used and/or analyzed during the current study are available from the corresponding author on reasonable request.

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References

- Rostami, M., Koushanfar, F. & Karri, R. A Primer on hardware security: Models, methods, and metrics. *Proc. IEEE* **102** (8), 1283–1295 (2014).
- Sengupta, A., Bhaduria, S. & Mohanty, S. P. TL-HLS: Methodology for low cost hardware trojan security aware scheduling with optimal loop unrolling factor during high level synthesis. *IEEE Trans. Comput. Aided Des. Integr. Circ. Syst.* **36** (4), 655–668 (2017).
- Sengupta, A., Roy, D., Mohanty, S. P. & Corcoran, P. DSP design protection in CE through algorithmic transformation based structural obfuscation. *IEEE Trans. Consum. Electron.* **63** (4), 467–476 (2017).
- Sengupta, A., Roy, D. & Mohanty, S. P. Triple-phase watermarking for reusable IP core protection during architecture synthesis. *IEEE Trans. Comput. Aided Des. Integr. Circ. Syst.* **37** (4), 742–755 (2018).
- Zhao, Y. et al. Side channel security oriented evaluation and protection on hardware implementations of Kyber. *IEEE Trans. Circuits Syst. I Regul. Pap.* **70** (12), 5025–5035 (2023).
- Kim, D., Hong, J. P., Lee, J. & Nam, J. W. High-speed light detection sensor for hardware security in standard CMOS technology. *IEEE Trans. Circ. Syst. II: Express Br.* **70** (10), 3917–3921 (2023).
- Yasin, M., Rajendran, J. J., Sinanoglu, O. & Karri, R. On improving the security of logic locking. *IEEE Trans. Comput. Aided Design Integr. Circ. Syst.* **35** (9), 1411–1424 (2016).
- Roy, D. B., Bhasin, S., Nikolić, I. & Mukhopadhyay, D. Combining PUF with RLUTs: A two-party pay-per-device IP licensing scheme on FPGAs. *ACM Trans. Embed. Comput. Syst.* **18** (2), 22 (2019).
- Yilmaz, Y., Aniello, L. & Halak, B. ASSURE: A hardware-based security protocol for resource-constrained IoT systems. *J. Hardw. Syst. Secur.* **5**, 1–18 (2021).
- Anandakumar, N. N. et al. Tehranipoor, rethinking watermark: Providing Proof of IP Ownership in Modern SoCs. *Cryptology ePrint Archive*. <https://eprint.iacr.org/2022/092> (2022)
- Das, U., et al. PSC-watermark: Power side channel based IP watermarking using clock gates. 2023 *IEEE European Test Symposium (ETS)*, Venezia, Italy, 1–6 (2023)
- Slpsk, P., Nair, A. A., Rebeiro, C. & Bhunia, S. SIGNED: A challenge–response scheme for electronic hardware watermarking. *IEEE Trans. Comput.* **72** (6), 1763–1777 (2023).
- Chen, J. & Schafer, B. C. *Watermarking of Behavioral IPs: A Practical Approach*, 2021 *Design, Automation & Test in Europe Conference & Exhibition (DATE)*, 1266–1271 <https://doi.org/10.23919/DAT51398.2021.9474071>. (2021)
- Koushanfar, F., Hong, I. & Potkonjak, M. Behavioral synthesis techniques for intellectual property protection. *ACM Trans. Des. Autom. Electron. Syst.* **10** (3), 523–545 (2005).
- Castillo, E., Parrilla, L., Garcia, A., Meyer-Baese, U., Botella, G. & Lloris, A. Automated signature insertion in combinational logic patterns for HDL IP core protection. 2008 *4th Southern Conference on Programmable Logic* 183–186 (2008)
- Yu, T. & Zhu, Y. A new watermarking method for soft IP protection. 2011 *International Conference on Consumer Electronics, Communications and Networks (CECNet)*. 3839–3842 (2011).
- Sengupta, A. & Rathor, M. Facial biometric for securing hardware accelerators. *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.* **29** (1), 112–123 (2021).
- Sengupta, A. & Rathor, M. IP core steganography for protecting DSP kernels used in CE systems. *IEEE Trans. Consum. Electron.* **65** (4), 506–515 (2019).
- Sengupta, A. & Chaurasia, R. Securing IP cores for DSP applications using structural obfuscation and chromosomal DNA impression. *IEEE Access* **10**, 50903–50913 (2022).
- Rathor, M., Sengupta, A., Chaurasia, R. & Anshul, A. Exploring handwritten signature image features for hardware security. *IEEE Trans. Depend. Secure Comput.* **20** (5), 3687–3698 (2023).
- Sengupta, A., Kumar, E. R. & Chandra, N. P. Embedding digital signature using encrypted-hashing for protection of DSP cores in CE. *IEEE Trans. Consum. Electron.* **65** (3), 398–407 (2019).
- Proteins & Amino Acid, Chapter 5, Harvard University, USA. (accessed on December 2024); https://projects.iq.harvard.edu/files/lifescienceslabookv1/files/5_-_proteins_and_amino_acids_revised_9-24-2018.pdf.
- Sengupta, A. & Anshul, A. Secure hardware IP of GLRT cascade using color interval graph based embedded fingerprint for ECG detector. *Nat. Sci. Rep.* **24**, 13250 (2024).
- Guo, Z., Karimian, N., Tehranipoor, M. M. & Forte, D. Hardware security meets biometrics for the age of IoT. *IEEE International Symposium on Circuits and Systems (ISCAS)* 1318–1321 (2016).
- Duong, V. A., Park, J. M., Lim, H. J. & Lee, H. Proteomics in forensic analysis: Applications for human samples. *Appl. Sci.* **11**, 3393 (2021).
- Abdelaziz, G. Synthesis of human insulin gene in vitro through computational methodology. *Life Sci. J.* **11**, 27–34 (2014).
- Sengupta, A., Roy, D., Mohanty, S. P. & Corcoran, P. Low-cost obfuscated JPEG CODEC IP core for secure CE hardware. *IEEE Trans. Consum. Electron.* **64** (3), 365–374 (2018).
- NanGate 15 nm Open Cell Library. (accessed on Dec 2024); <http://www.nangate.com/?pageid=2328>.
- Sengupta, A. & Chaurasia, R. Secure implantable cardiac pacemaker for medical consumer electronics. *NPJ Biomed. Innov.* <https://doi.org/10.1038/s44385-025-00008-y> (2025).
- Express benchmark suite, University of California Santa Barbara (UCSB), (accessed on February 2025); <https://web.ece.ucsb.edu/EXPRESS/benchmark/>
- Hu, W. et al. An overview of hardware security and trust: Threats, countermeasures, and design tools. *IEEE Trans. Comput. Aided Des. Integr. Circ. Syst.* **40** (6), 1010–1038 (2021).
- Potkonjak, M. *Methods and Systems for the Identification of Circuits and Circuit Designs* (Google Patents, 2006).
- Rathor, M. & Rathor, G. P. Hard-sign: A hardware watermarking scheme using dated handwritten signature. *IEEE Design Test* **41** (2), 75–83 (2024).
- Karmakar, R., Jana, S. S. & Chattopadhyay, S. A cellular automata guided finite-state-machine watermarking strategy for IP protection of sequential circuits. *IEEE Trans. Emerg. Topics Comput.* **10** (2), 806–823 (2022).

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Author contributions

Anirban Sengupta—Idea generation, development, writing, project supervision Nabendu Bhui—experiment and writing Vishal Chourasia—experiment and writing.

Declarations

Competing interests

The authors declare no competing interests.

Approval for human experiments

We confirm that all methods were carried out in accordance with relevant guidelines and regulations. We confirm that all experimental protocols were approved by Indian Institute of Technology Indore. We confirm that informed consent was obtained from all subjects and/or their legal guardian(s).

Additional information

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